

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	((("5059777") or ("5834750") or ("6286762")).PN.	US-PGPUB ; USPAT	OR	OFF	2005/04/06 14:01
S2	1	("6655591").PN.	US-PGPUB ; USPAT	OR	OFF	2005/04/06 14:01
S3	19	("4240064" "4967132" "5047699" "5059777" "5180904" "5212370" "5308962" "5410141" "5511205" "5747786" "5811774" "5834750" "5834760" "5942743" "5984187" "6089456" "6098877" "6150948" "6286762").PN.	US-PGPUB ; USPAT, USOCR	OR	OFF	2005/04/06 14:02
S4	0	("6655591").URPN.	USPAT	OR	OFF	2005/04/06 14:44
S5	772	(power current) near3 (provid\$3 consum\$3 draw\$3) near5 (sens\$3 detect\$3 measur\$3) near5 (master cpu processor controller)	US-PGPUB ; USPAT, USOCR	OR	OFF	2005/04/06 15:04
S6	190	((power current) near3 (provid\$3 consum\$3 draw\$3) near5 (sens\$3 detect\$3 measur\$3) near5 (master cpu processor controller)) same (regulat\$3 sleep reduc\$4 off down standby)	US-PGPUB ; USPAT, USOCR	OR	OFF	2005/04/06 15:37
S7	57	((power current) near3 (provid\$3 consum\$3 draw\$3) near5 (sens\$3 detect\$3 measur\$3) near5 (master cpu processor controller)) same ((regulat\$3 sleep reduc\$4 off down standby) near5 (slave circuit\$2 component\$1 device))	US-PGPUB ; USPAT, USOCR	OR	OFF	2005/04/06 16:21
S8	8	((power current) near3 (provid\$3 consum\$3 draw\$3) near5 (sens\$3 detect\$3 measur\$3) near5 (master cpu processor controller)) same ((regulat\$3 sleep reduc\$4 off down standby) near5 (slave circuit\$2 component\$1 device))	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/06 16:22

S9	91	fureman-\$.xa. and card	US-PGPUB ; USPAT; USOCR	OR	OFF	2005/04/07 13:31
S10	12	("4916333" "4932053" "5243648" "5847447" "5987614" "5995629" "5998978" "6064740" "6276609" "6278783" "6298135" "6327661").PN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2005/04/07 13:35
S11	6	("6419159").URPN.	USPAT	OR	OFF	2005/04/07 14:06
S12	2	(sens\$3 detect\$3) near5 (processor controller cpu mcu) near5 sleep\$3 near5 (slave peripheral)	USPAT	OR	OFF	2005/04/07 14:08
S13	148	(sens\$3 detect\$3) near5 (processor controller cpu mcu) near5 sleep\$3	USPAT	OR	OFF	2005/04/07 14:09
S14	33	(sens\$3 detect\$3) near5 (processor controller cpu mcu) near5 sleep\$3 near5 (current power amp\$1 amp\$1 microamp\$5)	USPAT	OR	OFF	2005/04/07 15:14
S15	12	(sens\$3 detect\$3) near5 (processor controller cpu mcu) near5 sleep\$3 near5 (current power amp\$1 amp\$1 microamp\$5)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/07 14:43
S16	1	"5511205".pn. and (current power amp\$1 amp\$1 microamp\$5)	USPAT	OR	OFF	2005/04/07 15:32
S17	1	"5511205".pn. and (current load\$1 amp\$1 amp\$1 microamp\$5)	USPAT	OR	OFF	2005/04/07 15:43
S18	1	"5511205".pn. and ("264" with "252")	USPAT	OR	OFF	2005/04/07 15:59
S19	1	"5511205".pn. and (load)	USPAT	OR	OFF	2005/04/07 15:59